

AUG 30 2005

TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
SEC.1111

In Re Application Of: Jong-Wook PARK et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/776,600	12 February 2004	YOHA, Connie C.	20987	2827	3485

Title: INTEGRATED CIRCUIT HAVING A NON-VOLATILE MEMORY CELL TRANSISTOR AS A FUSE DEVICE

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

in the above identified application.

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Signature

Dated: 30 August 2005

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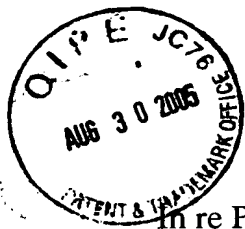
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Jong-Wook PARK et al.

Group Art Unit: 2827

Application No. 10/776,600

Examiner: YOHA, Connie C.

Filed: 12 February 2004

For: INTEGRATED CIRCUIT HAVING A NON-VOLATILE MEMORY CELL
TRANSISTOR AS A FUSE DEVICE

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Amendment
Randolph Building
401 Dulany Street
Alexandria VA 22314

Sir:

In response to the Office Action of 1 August 2005, Applicants elect the invention of Group I, Claims 1-4, drawn to integrated circuit having non-volatile memory cells having floating gate and gate control adapted to store or program status information. The election is without traverse.

Respectfully submitted,

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